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Inverse design and implementation of a wavelength demultiplexing grating coupler

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Nanophotonics has emerged as a powerful tool for manipulating light on chips. Almost all of today's devices, however, have been designed using slow and ineffective brute-force search methods, leading in many cases to limited device performance. In this article, we provide a complete demonstration of our recently proposed inverse design technique, wherein the user specifies design constraints in the form of target fields rather than a dielectric constant profile, and in particular we use this method to demonstrate a new demultiplexing grating. The novel grating, which has not been developed using conventional techniques, accepts a vertical-incident Gaussian beam from a free-space and separates O-band (1300 nm) and C-band (1550 nm) light into separate waveguides. This inverse design concept is simple and extendable to a broad class of highly compact devices including frequency filters, mode converters, and spatial mode multiplexers.

Conventional integrated photonic devices¹ include components such as waveguide directional couplers², multimode interference couplers³, distributed Bragg reflectors⁴, micro-ring resonators⁵, adiabatic tapers⁶ and grating couplers⁷. In all of these cases, the design space spans a relatively small (~ 2 – 5) number of parameters such as structure widths, heights and periodicity that are tuned throughout the photonic device design stage. To design a device, the photonic engineer specifies a dielectric profile, computes the electromagnetic field response using Maxwells equations, and compares the response to the device specifications. The process is then repeated, modifying the dielectric profile each iteration, until satisfactory performance is obtained. This brute force approach suffers from a long device design cycle, and does not take full advantage of the available design space of fabricable devices.

To this end, a wide variety of increasingly sophisticated approaches have been developed to search through this parameter space and optimize specific nanophotonic structures. Several methods, namely genetic algorithms^{8–10} and particle swarm optimization¹¹, ignore the underlying physics but have achieved considerable success in fine-tuning existing structures, and designing photonic crystal devices with selectively removed holes and posts. These methods, however, are typically restricted to optimizing a relatively small number of geometric parameters, and scale poorly with additional degrees of freedom. Other methods exploit the underlying physics to quickly converge on local optima, typically by computing the local gradient of a performance metric and using steepest-descent optimization¹². Owing to their much faster convergence, they can be used to design more complex structures with arbitrary topologies^{13–19}. These algorithms can search through the design space of complex, aperiodic structures beyond those we can come up with based on our intuition and experience. Such devices may be able to provide novel functionality, or higher performance and smaller footprints than traditional devices, due to the greatly expanded design space.

We have recently proposed an inverse design approach for linear optical components, where the user specified input is not the basic structure of the device, but rather a set of performance metrics¹⁹. These include the device area, the modes of input and output waveguides to use (e.g. TE, TM, and mode order), extinction ratios (e.g. >20 dB), and insertion loss (e.g. <1 dB). More rigorously, the user only specifies the coupling efficiencies between a set of input and output modes at various frequencies. Any linear optical device can be specified in this fashion²⁰, including mode converters, spatial mode multiplexers, and wavelength demultiplexers.

Two of the most important functions in integrated photonics, for both chip-to-chip and intra-chip optical interconnects, are wavelength-division multiplexing (WDM) and vertical-incidence coupling²¹. A device combining these two functions in a single, compact layout could be particularly useful for coupling between silicon

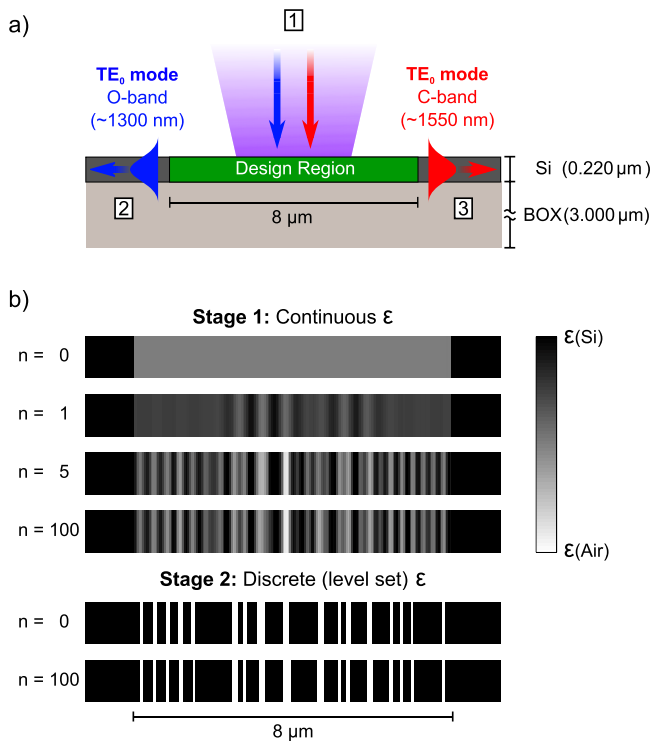


Figure 1 | The inverse design procedure. (a) The device specifications provided to the inverse design algorithm, which consist of the input and output modes, the design region, and the surrounding structure. The device is fabricated by fully etching a 220 nm silicon layer on 3 μm of buried oxide, in the pattern produced by the optimization algorithm (see figure 1b). (b) Intermediate steps in the optimization process, where n is the iteration number. The optimization process proceeds in two stages. In the first stage, the permittivity ϵ is allowed to vary continuously. In the second stage, the design is converted to a binary level-set representation and fine-tuned. To clearly illustrate the design process, the diagrams are not to scale in the vertical direction.

photonic layers in a stacked-die microprocessor, or for coupling on/off chip using optical fibers. Although a uniform grating coupler with a tilted incident beam will act as a wavelength-demultiplexing grating

coupler for two wavelength bands²², the symmetry imposed by a vertically incident beam implies that a uniform grating coupler cannot split wavelengths. Indeed, an efficient vertically-incident wavelength-demultiplexing grating coupler cannot be designed using current analytic methods, or by tuning a small number of parameters by hand.

Here, we provide a full demonstration of the inverse design technique's power by experimentally demonstrating a vertical-incidence, wavelength-demultiplexing grating coupler fabricated in silicon-on-insulator (SOI). The grating accepts a vertically incident beam from free space or an optical fiber, and splits O-band (~ 1300 nm) and C-band (~ 1550 nm) light into separate silicon photonic waveguides with high extinction ratios (>10 dB).

Results

In general, we can specify the performance of a linear optical device by defining the mode conversion efficiency between a set of input modes and output modes²⁰. These modes are specified by the user, and kept fixed during the optimization process. The input modes $i = 1 \dots M$ are at frequencies ω_i and can be represented by equivalent current density distributions \mathbf{J}_i . The generated electric fields \mathbf{E}_i should satisfy Maxwell's equations in the frequency domain,

$$\nabla \times \mu_0^{-1} \nabla \times \mathbf{E}_i - \omega_i^2 \epsilon \mathbf{E}_i = -i\omega_i \mathbf{J}_i, \quad (1)$$

where ϵ is the electric permittivity, and μ_0 is the magnetic permeability of free space.

We can then specify N_i output modes of interest for each input mode i . The output mode electric fields \mathcal{E}_{ij} are given over output surfaces S_{ij} , where $j = 1 \dots N_i$. The amplitude of each output mode should be bounded between α_{ij} and β_{ij} , which can be expressed as

$$\alpha_{ij} \leq \left| \iint_{S_{ij}} \mathcal{E}_{ij}^* \cdot \mathbf{E}_i dS \right| \leq \beta_{ij} \quad (2)$$

for $i = 1 \dots M$ and $j = 1 \dots N_i$. We are thus interested in finding ϵ and \mathbf{E}_i which simultaneously satisfy (1) and (2).

The WDM grating was designed by specifying the input mode to be a 4.4 μm diameter vertically-incident Gaussian beam, and the output modes to be the fundamental TE mode of the output silicon slab waveguides, as shown in figure 1a. The algorithm was directed to maximize power into the left waveguide and minimize power into the right waveguide at 1300 nm, and the converse at 1550 nm.

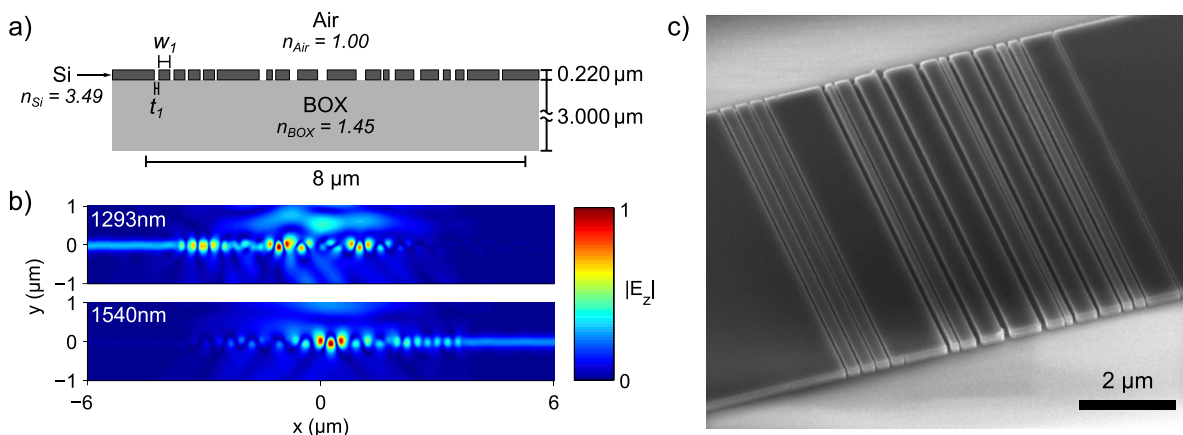


Figure 2 | Basic structure of the wavelength demultiplexing grating. (a) The device consists of a 220 nm silicon layer with fully etched trenches on top of 3 μm of buried oxide. Dimensions of the trench widths (t_1) and spacings (w_1), chosen by the optimization algorithm, are listed in Supplementary Table 1. (b) Frequency-domain electric field amplitudes for a 4.4 μm diameter Gaussian beam incident on the center of the grating. At 1293 nm, light is only coupled into the fundamental mode of the left waveguide, whereas at 1540 nm, light is only coupled into the right waveguide. The fields plotted here were calculated using a finite-difference time-domain simulation, and post-processed using narrowband frequency filters to obtain the phasor fields. (c) Scanning electron microscopy (SEM) image of the fabricated device.



Given the design specifications, our algorithm iteratively optimizes the structure using a close analogue of steepest-descent optimization to meet the constraints given in equation (2), as detailed in the Supplementary Information. We use finite-difference frequency domain (FDFD) simulations to calculate the local gradient during each step^{23,24}. As illustrated in figure 1b, the design procedure consists of two stages. In the first stage, the permittivity ϵ is allowed to smoothly vary within the design region. In the second stage, we convert the structure to a level-set representation²⁵ and fine-tune the final structure. We did not apply any design rules such as a minimum feature size, although such constraints could be incorporated into the design process¹⁹. The entire inverse design process for this grating took only ~ 15 minutes on a single Intel Core i7 processor.

The final device was fabricated in SOI with a 220 nm thick Si device layer and a 3 μm BOX (buried oxide) layer by fully etching the Si device layer, as described in figure 2a. The dimensions and locations of the grating trenches are detailed in Supplementary Table 1. The broadband performance of the device was verified by using 2D finite-difference time domain (FDTD) simulations. The phasor fields at 1293 nm and 1540 nm for a 4.4 μm diameter Gaussian beam, obtained using 2D-FDTD simulations, are plotted in figure 2b. The fabricated device is presented in figure 2c.

The final fabricated structures also incorporate a waveguide and curved broadband output grating on either side of the wavelength-demultiplexing grating, as shown in figure 3a. Scanning electron microscopy (SEM) images of the fabricated structures are shown in figure 3b. The output gratings were strongly chirped to provide broadband performance. To minimize Fabry-Perot resonances due to back-reflections, the output gratings were slightly curved and placed far from the ends of the waveguides (inset). In future, the output waveguides could be edge-coupled to optical fibers to obtain a well-characterized out-coupling efficiency²⁶.

The WDM grating was excited in the vertical direction by a focused Gaussian beam with a diameter of $\sim 4 \mu\text{m}$, and a fraction of the coupled light was out-coupled by the output gratings. To ensure a clean Gaussian input beam, the source was passed through a length of single-mode optical fiber. Spectroscopic data was measured using a broadband LED source, and narrowband images were taken using tunable lasers. The structure was both excited and measured through a single plan-apochromat microscope objective integrated into a custom confocal microscopy setup. The collected light was either directly imaged using an InGaAs near-infrared camera, or spatially filtered by a pinhole at a focal plane and analyzed using a grating spectrometer with an InGaAs detector.

Images of the device broadly illuminated with white light, and excited by a focused tunable laser are presented in figure 3c–e. At 1320 nm, light is only coupled to the left output grating, and at 1540 nm, light is only coupled to the right output grating, clearly demonstrating the basic functionality of the device.

In figure 4, we present both the simulated and experimentally measured coupling efficiency spectra of the WDM grating. The coupling efficiencies of the WDM grating computed using 2D FDTD are plotted in figure 4a. At 1293 nm, the simulated coupling efficiencies into the left and right waveguides are 0.2937 and 0.0008 respectively, whereas at 1540 nm, the coupling efficiencies are 0.0027 and 0.4544. The measured signal intensity, normalized to source brightness, from the two output gratings is plotted in figure 4b. The output grating coupling efficiencies were not measured, but due to the symmetry of the experimental setup, measurements of the two output ports are directly comparable. The experimental data from 1350 nm–1450 nm is shaded to due to the presence of strong atmospheric water absorption lines²⁷. Fabry-perot fringing, arising from reflections between the WDM grating and output gratings, is visible in the measured spectra, with fringe spacing corresponding to the spacing between the gratings. The

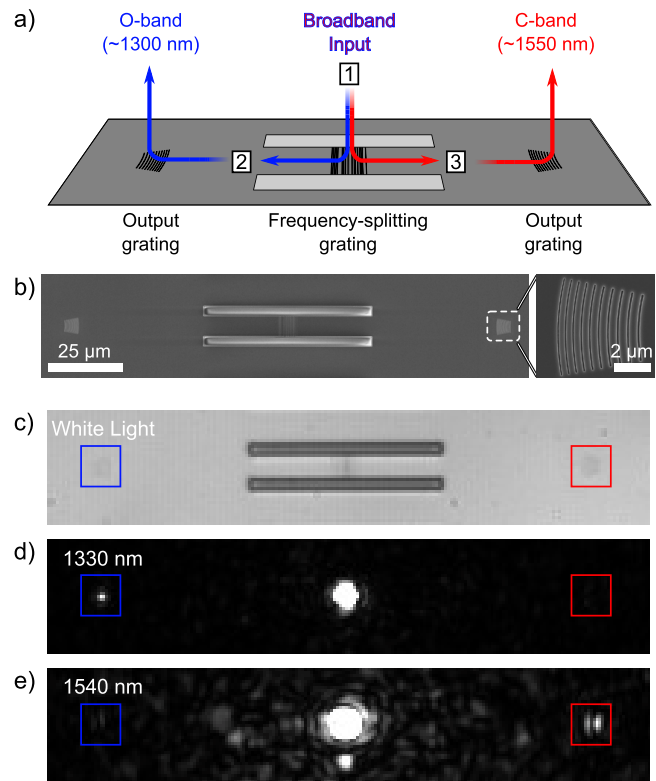


Figure 3 | Experimental configuration used to measure the WDM grating coupler. (a) The WDM grating is excited by a free-space beam, which couples light into the silicon slab waveguide. The coupled light is then scattered upwards by the two identical output gratings, and collected via free-space optics. (b) Scanning electron microscopy (SEM) images of the fabricated structures. The wavelength-demultiplexing grating coupler is in the center of a 8 μm wide, 70 μm long waveguide, and the two output grating couplers are placed 50 μm from the ends of the waveguide. The output grating couplers are strongly chirped to provide broadband coupling, and slightly curved to minimize back-reflections into the waveguide. (c–e) Infrared camera images of the device under (c) broad white-light illumination, and when the WDM grating is excited by a focused laser beam tuned to (d) 1320 nm and (e) 1540 nm. The locations of the two output gratings are indicated by the colored boxes. Light is only coupled to the left output grating at 1320 nm, and only the right grating at 1540 nm. Back-scatter from the rear surface of the wafer is visible near the center of both images.

measured signals broadly match the simulated coupling efficiencies of the WDM grating.

The splitting ratio, defined as the ratio of power emitted from the two output ports, is plotted in figure 4c. The fabricated device has a measured splitting ratio of 17 ± 2 dB at 1310 nm and 12 ± 2 dB at 1540 nm, whereas the designed values at these wavelengths were 19.6 dB and 22.2 dB and respectively. The discrepancy between the simulated and measured splitting ratio is likely due to fabrication imperfections.

In conclusion, we have provided the first experimental demonstration of a nanophotonic device designed using our inverse design algorithm. In particular, we have implemented an efficient vertical-incidence wavelength-demultiplexing grating coupler, which cannot be designed by hand or by using parameter sweeps. By vastly opening up the parameter space for nanophotonic devices, inverse-design has broad implications for the future design of novel and compact nanophotonic components with full three-dimensional freedom.

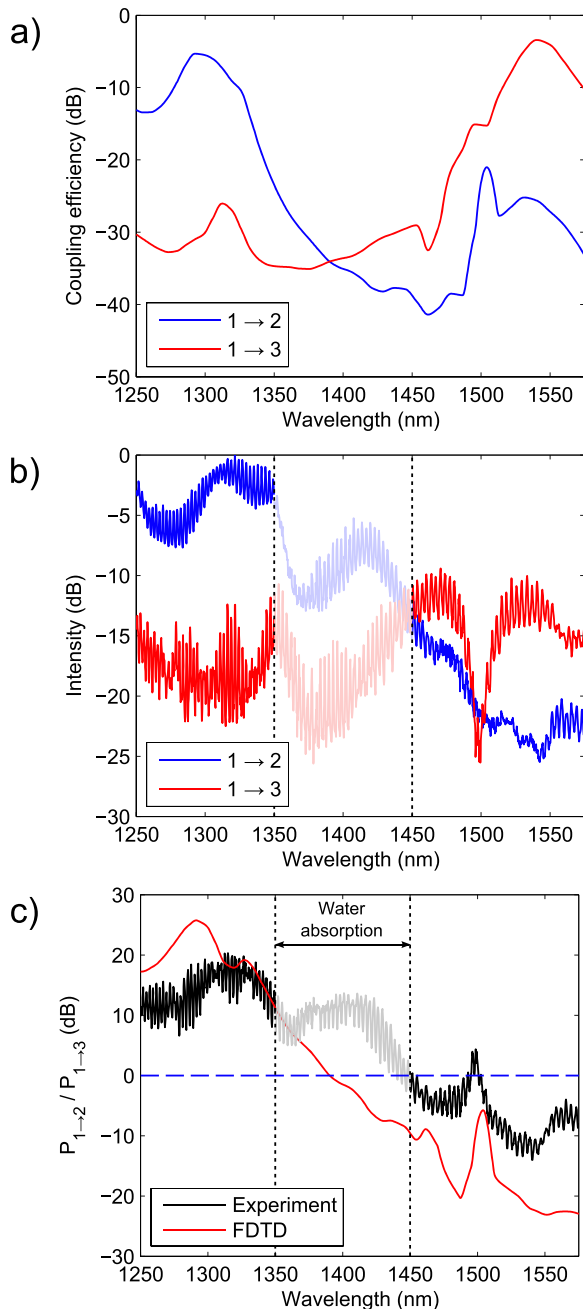


Figure 4 | Simulated and measured coupling efficiencies of the WDM grating coupler. The device ports are labelled as in figure 3a. (a) Simulated coupling efficiency into the left and right waveguides, calculated using a finite-difference time domain (FDTD) simulation on the structure with optimized parameters. A $4.4\ \mu\text{m}$ diameter Gaussian beam was used as the input. (b) Experimental measured intensities from the left and right output gratings. The intensities are only normalized with respect to the source since the output grating efficiencies are not known. Around 1300 nm, light is predominantly coupled to the left grating, whereas around 1550 nm, light is predominantly coupled into the right grating. Measurements in the 1350–1450 nm band (shaded) are corrupted by water absorption lines in the atmosphere. (c) Simulated and experimentally measured splitting ratios for the coupler, defined as the ratio of power into the left and right waveguides. We have experimentally measured splitting ratios of 17 ± 2 dB at 1310 nm and 12 ± 2 dB at 1540 nm.

Methods

Optimization algorithm and electromagnetic simulations. Our inverse design algorithm uses a close analogue of steepest-descent optimization, and incorporates the MaxwellFDTD finite-difference frequency-domain solver to calculate local gradients^{23,24}, as detailed in the Supplementary Information.

After completing the design process, the broadband performance of the device was calculated using 2D finite-difference time-domain (FDTD) simulations.

Fabrication. The detailed grating dimensions are listed in Supplementary Table 1. The devices were fabricated on Unibond™ SmartCut™ silicon-on-insulator (SOI) wafers obtained from SOITEC, with a nominal 220 nm device layer and $3.0\ \mu\text{m}$ BOX layer. A JEOL JBX-6300FS electron beam lithography system was used to pattern 330 nm of ZEP-52A electron beam resist spun on the samples. The pattern was then transferred to the Si device layer with a magnetically-enhanced reactive-ion etcher using a HBr/Cl_2 chemistry. Finally, the mask was stripped by sonicating in Microposit remover 1165.

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Author contributions

A.Y.P. designed and fabricated the structures, simulated their performance, and performed the experiments. J.L. developed the inverse-design algorithm used in this paper. J.V. and T.B. provided theoretical and experimental guidance, and J.V. supervised the entire project. K.G.L. and J.P. provided experimental support. All authors contributed to discussions.

Additional information

Supplementary information accompanies this paper at <http://www.nature.com/scientificreports>

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